



K. J. Somaiya College of Engineering, Mumbai-77

(Autonomous College Affiliated to University of Mumbai)

SECOND HALF - 2019

S.E.(SEM. III) ESE THEORY EXAMINATION TIME TABLE

(MU Non CBGS Syllabus Under Autonomy with Equivalent Courses from KJSCE 2014)

Candidates appearing for the above examination are requested to be present at the examination hall, fifteen minutes before the commencement of the examinations and will not be allowed after the half an hour of the commencement of the examinations. The candidates are required to carry the hall ticket / college Identity Card. THEY ARE FORBIDDEN TO CARRY ANY ELECTRONICS GADGETS LIKE MOBILE PHONE, SMART WATCH, ETC., ANY BOOKS, NOTES OR PAPER ETC. IN THE EXAMINATION HALL. Seat numbers will be displayed on the college notice boards before the date of commencement of the examination and seating arrangement will be displayed on college notice boards on the day of examination. The ESE examinations of S.E. (SEM – III) will be conducted as per the following schedule: -

DAY	MONDAY	TUESDAY	THURSDAY	SATURDAY	TUESDAY	THURSDAY
DATE	25 November 2019	26 November 2019	28 November 2019	30 November 2019	3 December 2019	5 December 2019
TIME	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM
COMPUTER ENGINEERING	Electronics Devices And Linear Circuits	Applied Mathematics- III <i>Equivalent for Applied Mathematics-III</i>	<i>Data Structure Equivalent for Data Structure and Files</i>	Computer Organization and Architecture <i>Equivalent for Computer Organization & Architecture</i>	Digital Logic Design And Application	Discrete Structure and graph Theory <i>Equivalent for Discrete Structure and graph Theory</i>
ELECTRONICS ENGINEERING	---	Applied Mathematics III <i>Equivalent for Engineering Mathematics III</i>	Basic Electronics Circuits- <i>Equivalent for Basics of Electronic Circuits</i>	Digital Design <i>Equivalent for Digital System Design-I</i>	---	Electrical Networks <i>Equivalent for Electrical Network Analysis And Synthesis ---</i>

Candidates are permitted to use the scientific but not programmable calculator at the examination. Candidates for the examination are expected to provide themselves with Data Books and drawing instruments if required. The exchange or loan of Data Books, drawing instruments or any other materials used in the examination is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances. Any unfair means in the examination hall during the theory examination is punishable as per the rules and regulations.

Courses Printed in bold wherein there is no equivalent course given

Mumbai – 400 077

DATE: - 5 November 2019

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DAY	MONDAY	TUESDAY	THURSDAY	SATURDAY	TUESDAY	THURSDAY
DATE	25 November 2019	26 November 2019	28 November 2019	30 November 2019	3 December 2019	5 December 2019
TIME	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM
ELECTRONICS AND TELECOMMUNICATION ENGINEERING	---	Applied Mathematics-III <i>Equivalent for Applied Mathematics III</i>	Analog Electronics I <i>Equivalent for Electronic Devices And Circuits-I</i>	Digital Logic Design <i>Equivalent for Digital Logic Design</i>	Electronic Instrumentation	<i>Electrical Networks Equivalent for Electrical Networks</i>
INFORMATION TECHNOLOGY	---	Applied Mathematics III <i>Equivalent for Applied Mathematics III</i>	Data Structure <i>Equivalent for Data Structure & Algorithms</i>	Database Management System <i>Equivalent for Gui & Database Management</i>	Digital Logic Design and Applications <i>Equivalent for Digital Logic Design And Application</i>	Electronics Devices And Linear Circuits

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S.E.(SEM. III) ESE THEORY EXAMINATION TIME TABLE SECOND HALF – 2019 (MU Non CBGS Syllabus Under Autonomy with Equivalent Courses from KJSCE 2014)

Candidates appearing for the above examinations are requested to be present at the examination hall, **fifteen minutes** before the commencement of the examinations and will not be allowed after the half an hour of the commencement of the examinations. The candidates are required to carry the hall ticket / college Identity Card. **THEY ARE FORBIDDEN TO CARRY ANY ELECTRONICS GADGETS LIKE MOBILE PHONE, SMART WATCH, ETC., ANY BOOKS, NOTES OR PAPER ETC. IN THE EXAMINATION HALL.** Seat numbers will be displayed on the college notice boards before the date of commencement of the examination and seating arrangement will be displayed on college notice boards on the day of examination. The ESE examinations of S.Y. B. Tech (SEM – IV) will be conducted as per the following schedule: -

DAY	MONDAY	TUESDAY	THURSDAY	SATURDAY	TUESDAY	THURSDAY
DATE	25 November 2019	26 November 2019	28 November 2019	30 November 2019	3 December 2019	5 December 2019
TIME	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM
MECHANICAL ENGINEERING	---	Applied Mathematics III <i>Equivalent for Applied Mathematics III</i>	Strength of Materials <i>Equivalent for Strength Of Materials</i>	Machine Drawing (1.30 Pm To 5.30 Pm)	Thermodynamics <i>Equivalent for Thermodynamics</i>	Production Process I <i>Equivalent for Production Process-I</i>

DAY	THURSDAY
DATE	12 Dec 2019
TIME	2.30PM-5.30 PM
ELECTRONICS ENGINEERING	Control System Engineering (of Semester IV) <i>Equivalent for Control System</i>

Candidates are permitted to use the scientific but not programmable calculator at the examination. Candidates for the examination are expected to provide themselves with Data Books and drawing instruments if required. The exchange or loan of Data Books, drawing instruments or any other materials used in the examination is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances. Any unfair means in the examination hall during the theory examination is punishable as per the rules and regulations.

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